A Fast and Rigorously Parallel Surface Voxelization Technique for GPU-Accelerated CFD Simulations

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Abstract. This paper presents a fast surface voxelization technique for the mapping of tessellated triangular surface meshes to uniform and structured grids that provide a basis for CFD simulations with the lattice Boltzmann method (LBM). The core algorithm is optimized for massively parallel execution on graphics processing units (GPUs) and is based on a unique dissection of the inner body shell. This unique definition necessitates a topology based neighbor search as a preprocessing step, but also enables parallel implementation. More specifically, normal vectors of adjacent triangular tessellations are used to construct half-angles that clearly separate the per-triangle regions. For each triangle, the grid nodes inside the axis-aligned bounding box (AABB) are tested for their distance to the triangle in question and for certain well-defined relative angles. The performance of the presented grid generation procedure is superior to the performance of the GPU-accelerated flow field computations per time step which allows efficient fluid-structure interaction simulations, without noticeable performance loss due to the dynamic grid update.

AMS subject classifications: 51P05, 65Z05, 68W10

Key words: Fast Grid Generation, Parallel Cartesian Mapping, Voxelization, GPU, Lattice Boltzmann, ELBE.

1 Introduction

CFD methods rely on the discretization of the continuous governing equations into discrete, finite approximations, on either Lagrangian grids, Eulerian grids, or in meshless formulations. Lattice Boltzmann methods, as addressed in the present publication, discretize the governing equations on an equidistant Eulerian grid. Grid points of a computational domain used for Lattice Boltzmann CFD implementations can be of a variety of

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different kinds. Those points representing a fluid particle ensemble are considered \textit{fluid particles}, whereas other points can represent a slip wall, a no-slip wall, an inflow or even moving velocity boundaries. Since the grid itself is static and does not change over time, it is - apart from proper dynamic boundary conditions at the body surface - the changing character of the points through which a moving body’s motion is manifested. Fig. 1 shows fluid particles as black circles and solid body particles as red dots. Black circles overlaid with red dots signify solid body particles from previous time steps, where a lighter shade of red indicates solid body particles from iterations further back in time.

![Figure 1: At each time step a moving body (red arrow) has to be mapped to the Cartesian grid. Hues of red indicate the changing grid node domain occupied by the body over time.](image)

The grid generation algorithm that is presented in this paper is designed to be integrated seamlessly into a GPU-based CFD solver, the efficient Lattice Boltzmann environment \textit{ELBE} [1]. The \textit{ELBE} solver solves for three-dimensional turbulent free surface flows, including effects of viscosity and turbulent dissipation. Interactions of fluid and structure are considered as well. In transient CFD simulations with fluid-structure interactions, the solid body points have to be updated at each time step. The GPU-based implementation of \textit{ELBE} allows for very competitive simulation times. Hence, the performance of the grid update algorithm and its implementation is crucial for successful simulations of challenging FSI problems. The main goal of this contribution is to develop a grid generation algorithm, that is

- Real-time capable — to rapidly voxelize the surface of a geometric object into a surface voxel representation in (or near) real time to be included in near-real time CFD simulations.
- Error-free and robust — to minimize the errors and the number of misidentified voxels, a source of potential instabilities in the numerical simulation.
- Efficient and extensible — to be convenient to integrate into a wide range of other CFD applications, e.g. SPH solvers.

After a brief literature review in Section 2, the following sections give further details of the necessary surface mesh preparations, details of the concrete algorithm and a detailed analysis of the performance of the algorithm, including numerical experiments.
2 Related work and key ideas of the proposed algorithm

The current paper describes a method to voxelize triangle meshes for Lattice Boltzmann CFD simulations on Eulerian grids. However, because voxelization is a very important topic for all sorts of different applications, multiple such methods have been proposed in recent years. Many of these even focus on very efficient GPU implementations, as the underlying technique (voxelization) is — by nature — related to rendering purposes. In order to motivate the need for yet another GPU-accelerated grid generation technique, we briefly review the current state-of-the-art in voxelization in the following.

2.1 An attempt of classification

Key characteristics of the proposed voxelization algorithms have to be identified, in order to systematically review the relevant literature. Apart from the purpose of voxelization, which can be but is not limited to rendering, collision detection, or grid generation for numerical solvers, two characteristics can be identified: (i) the voxelization type (surface voxelization vs. solid voxelization), and (ii) the utilized acceleration techniques (software vs. hardware acceleration). This work’s main focus is on hardware accelerated surface voxelization.

2.1.1 Surface voxelization vs. solid voxelization

The most important feature of voxelization algorithms is the set of voxels that it creates. In case only the surface of objects is voxelized, *surface voxelization*, the algorithmic challenge is to identify as many voxels as possible to obtain a closed surface, but at the same time mark as little voxels as possible to safe computational costs and memory. The fundamentals of surface voxelization techniques have been discussed in e.g. Cohen-Or [2] and Huang [3]. The latter publication also includes a first basic algorithm for the voxelization of polygon meshes. Each mesh triangle is turned into a set of voxels: (i) inside certain well-defined spheres that surround the vertices, (ii) inside three cylinders that surround the edges, and (iii) between two planes defined by an offset $h$ from the base triangle. The topological approach of Laine [4] uses geometric intersection targets that are associated with each voxel to identify those specific voxels that can be considered as constituting the interface between the inside and outside domains. By utilization of differently shaped intersection targets, the connectivity of the generated voxel surface can be changed and a number of traditional voxelization techniques can be reproduced. However, this approach is non-directional and does not allow for an identification of inner nodes. Opposite to surface voxelization, *solid voxelization* methods identify all the interior voxels of a solid body. Solid voxelization is not in the scope of this work, as we’re interested in information on the boundary nodes only. For a detailed review on solid voxelization methods, the reader is referred to Liao [5].
2.1.2 Hardware acceleration

While the first publications on surface and solid voxelization were of theoretical nature, a lot of work was also conducted on a proper hardware acceleration of these techniques. Naturally, as many of the applications of such voxelization algorithms emerged from rendering and visualization tasks, graphics hardware was used very early to accelerate the voxelization process. E.g., Karabassi et al. [6] present a fast voxelization algorithm, which is based on the depth buffers (Z-buffer) of the graphics card. Their method can be accelerated by taking advantage of widely available, low-cost hardware. However, it can not reproduce inner cavities as they can not be seen from the depth buffers. Similarly, Fang and Chen [7] presented three solid/surface voxelization algorithms that have been accelerated with dedicated graphics hardware, the SGI Reality Engine. In 2006, Eisemann [8] presented a voxelization technique based on existing rendering steps in the graphics pipeline. The method employs a texture map, to store voxel information in an RGBA buffer and utilizes the accelerator’s resources efficiently but is limited to graphics applications where the $x,y$ dimensions are much larger then the $z$ dimension. It also suffers from occasional wholes in the voxelized surface due to its roots in rendering technology. While being very effective, such hardware-accelerated algorithms are optimized for specific graphics cards and lack the desired extensibility to new hardware at the same level of relative performance. Modern GPU programmability allows the same level of efficiency for much more generally applicable approaches.

Schwarz and Seidel [9] present CUDA implementations of data-parallel algorithms for both surface and solid voxelization. They present a new triangle/box overlap test and claim that their method outperforms previous GPU-based approaches by up to one order of magnitude. More recently, Rauwendaal and Bailey [10] presented a voxelization method based on the standard graphics pipeline and implemented it in OpenGL. They claim that their method can be integrated very well in existing OpenGL applications and that it is both robust and efficient. Pantaleoni’s [11] blending-based rasterization uses a two staged process of coarse and fine rasterization. Tiles are generated by a triangle/box overlap test and the containing triangles are sorted for faster processing in the fine raster step. The performance of this method is impressive and it represents a state of the art solution for surface voxelization.

2.2 Grid generation in the scope of Lattice Boltzmann solvers

Existing LBM grid generators include serial and parallel implementations of voxelization methods of various kind on both CPU and GPU architectures. However, the employed algorithms and models seem to be quite independent on the research that took place in the field of graphics processing and visualization, mostly due to historical reasons. One of the reasons might be that, up to now, most of the state-of-the-art Lattice-Boltzmann multiphysics solvers still are CPU-based, so that the published voxelization techniques (most of which were directly related to early GPU hardware) were not applicable. Nevertheless, the state-of-the-art in LBM grid generation will be briefly reviewed in the following.
The authors of [12] and [13] describe three distinct methods for the LBM grid generation around complex three-dimensional bodies and apply them for 3D fluid-structure interaction problems with an LBM-CPU solver on octree-type grids: (i) half-plane based algorithms, that are based on the fact that for convex geometries, all edges between arbitrary pairs of vertices are located inside the geometry. One of the main drawbacks of this method consequently is the limitation to convex polyheders [14]; (ii) the solid-angle-algorithm, that sums up the relative angles from the point under consideration to each polyeder vortex. The result indicates if the point is inside or outside the polyeder, see [15]. The method potentially can be extended to 3D [16], but is very inefficient and does not necessarily identify grid points that are exactly located on the polyeder edges; (iii) ray crossing algorithms, that emit a number of rays from the point under consideration and count the ray intersections with the polyeder surface [17]. Odd or even numbers of ray intersections with the surface indicate the inside or the outside, respectively, of a closed body. However, this method suffers from poor scalability for large numbers of triangles. Moreover, additional care has to be taken for the special case that the ray pierces a grid node and multiple triangles are intersected. In this case, the number of ray intersections is meaningless because it is not trivial to determine if the ray changed its inside/outside state at that point, it might have merely touched the surface. Szucki and Suchy’s CPU approach on voxelization [18] uses a similar raycasting method, which creates a solid voxelization, essentially being based on the work of Thon [19]. Odd or even numbers of ray intersections with the surface indicate the inside or the outside, respectively, of a closed body. Since their method’s performance decreases with a rising number of surface triangles and is of the order of seconds for just 10,000 triangles it is not suitable for real-time lattice updates. Inclan [20] pursued an approach based on a uniform and structured Cartesian grid produced by CartGen† from an STL (StereoLithography Interface Specification or Standard Tessellation Language) [21] file. The generated mesh blocks are filled with lattice nodes according to the applied boundary condition at the considered interface. All processing steps are executed subsequently and in serial on the CPU. The final output data is saved as a VTK (Visualization Toolkit) file and used by the LBM solver. All in all, the previously described techniques are all CPU-based and focus on solid voxelization.

2.3 Proposed algorithm: ePiP, an efficient point in polygon tester

The more recent hardware accelerated solutions reviewed in Section 2.1.2 can be used to generate Lattice Boltzmann grids efficiently. However, a tailor made solution was sought after by the authors of this work to meet the solver’s requirements in terms of accuracy and extensibility. Moreover, for a better compatibility with ELBE and other current GPU-accelerated flow solvers, instead of rendering pipelines and/or OpenGL, the nVIDIA CUDA programming model is to be used for the implementation.

†http://cartgen.sourceforge.net/
Opposite to most of the previously mentioned voxelization techniques, the requirements for voxelizations that are used to create our Lattice Boltzmann grids are very special. Ideally, only the first layer of inner body nodes is identified, allowing for a proper fulfillment of flow field boundary conditions and indirect addressing techniques, so that only surface voxelization techniques can be applied. Instead of the flow field calculations, the boundary nodes execute specific operations to satisfy the solid-body boundary conditions. If the surface layer is not closed, the surface is not watertight. If the surface layer is too thick, more boundary operations are executed than needed. Moreover, the presented grid generation algorithm could also be run in an inverse mode, identifying the first layer of fluid nodes in the computational domain. With a reformulation of solid-body boundary conditions, this would allow to remove all inner nodes from both the simulation and also the data structure, for maximum savings. All this is only possible with the help of a surface voxelization algorithm. In case that a solid voxelization is required in any intermediate simulation step, this still can be realized by some state-of-the-art flood fill algorithms. In the most-complete hydrodynamic LBM model, the D3Q27 LBM model, every lattice node interacts with its 26 next-neighbors, on equidistant Cartesian grids. Consequently, the voxelizations that are employed to produce the computational grids have to be 26-separating, in order to obtain a watertight surface. Opposite to previously suggested surface voxelization techniques, in addition to the 26-separability, all the identified voxels need to be in the interior of the solid body. Consequently, our new LBM grid generation process can be categorized as a hybrid surface- and solid-voxelization technique, identifying the outmost 26-separated layer of a solid voxelization of the geometric object. In terms of voxelization performance, race conditions and conflicting memory accesses that occur in standard overlap tests have to be avoided. Schwarz and Seidel [9] also identified this problem and used atomic functions and memory buffers in the voxel update. The algorithm that is presented here avoids the problem by construction.

In the scope of this paper, a novel, very efficient and reliable test is presented, that is designed for execution on massively parallel processors such as GPUs. On this kind of hardware, identical instructions are executed on different chunks of data, which is often referred to as SIMD (single instruction, multiple data) concept. The main design choice in the development of a novel algorithm is how to split up the global computational task into small and independent instruction sets that are then processed in parallel by a number of threads. In the proposed ePiP grid generation algorithm, each computational thread is assigned with one triangle. For each triangle a local region test within the axis-aligned bounding box of the triangle yields the identification of body points. To uniquely define the region of valid inner points for the triangles, it is necessary to construct separation planes between adjacent triangles, i.e. for each edge of any given triangle. With this information it is possible to assign unique parts of the body domain to triangles. As an example, consider the 2D case depicted in Fig. 2. In the two-dimensional domain three-dimensional triangles and planes resemble edges or lines (3D bodies resemble 2D areas). The gray body consists of five corner points and edges. Assuming a symmetrical partition of the domain, at each of these corner points (corner points in 2D correspond to
triangle edges in 3D) half-angles $\alpha$ can be defined in such a way that the angle between an edge and the separation plane equals $\alpha$ and the angle between two body edges (triangles) equals $2\alpha$. For efficiency reasons and to better apply boundary conditions to the body’s points, it is desirable to identify the first inner layer of body points only. In Fig. 2 the image on the lower left shows this first-layer region in blue, green, and pink colors. The detailed magnification of the blue-green corner depicts the half-angle definition. Red dots represent the first layer of solid body grid nodes.

3 Surface mesh preparations

The proposed grid generation algorithm is based on tessellated surface mesh information. Before the actual grid generation algorithm is triggered, several preliminaries are required. In order to construct the separation planes that are needed later in the grid generation process, the provided triangular surface mesh is preprocessed and information on the triangle connectivity is extracted from the given vertex information.

3.1 Topology based neighbor search for tessellated surfaces

In order to generate half-angles for the triangle edges, it is necessary to find each triangle’s neighbors. For any given closed and tessellated surface without bifurcations there exist three neighboring triangles to every triangle. In the example shown in Fig. 3 the blue triangle $t_1$ has three light blue neighbors $n(i, t_1)$ with $i = 1, 2, 3$. The gray triangles $n(j, n(i, t_1))$ are the respective neighbors thereof, where $j = 1, 2, 3$, $i = 1, 2, 3$, and $n(3, n(i, t_1)) = t_1$. With the blue triangle being the considered triangle, the light blue ones are first degree neighbors and the gray triangles are second degree neighbors. The out-
Figure 3: A triangle and its neighbors on a closed and tessellated surface.

lined surrounding triangles are neighbors of order three or higher. Fig. 3 represents all
the information a neighbor search operation should produce.

Arbitrary surfaces can be described with the widely used STL file format, containing
a list of triangle corner point locations. Repetitively, triangles are defined by stating their
vertex coordinates as three-tuples. For closed surfaces, this representation redundantly
stores vertices that are shared by multiple triangles. This not only introduces possible
gaps between triangles resulting from round off errors but also requires more memory
than actually necessary to store the well defined point positions and surface topology.
From the information stored in the STL file, a list of all occurring points and their respec-
tive triangle IDs is generated. Sorting this list in a $x-y-z$ hierarchy allows the identifica-
tion of point clusters. E.g. a cluster of five identical points indicates five triangles sharing
this point. The point clusters are used to derive the triangle connectivity necessary for
the definition of separation planes.

Consider the unit cube with its eight cornering points shown in Fig. 4. Each face of
the cube is divided into two triangles, generating twelve triangles with a total of 36 non-
unique points. After generating the list $\mathcal{L}$ from the STL information and $\mathcal{L}_{\text{points}}$ by sort-
ing it in space, the eight unique points are identified from the point clusters. The newID is
counting through the unique points, the longID is the point’s original ID and the shortID
stores the cluster’s first point’s original ID, i.e. its longID. Extracting the unique points
of $\mathcal{L}_{\text{points}}$ yields the list $\mathcal{L}$. The arrays $A_p$ and $A_l$ are used for the neighbor determination
algorithm. They reference points in $\mathcal{L}$ with their newID, can access point clusters through
the shortID and derive the triangle ID from a point’s longID. The neighbor determina-
tion process is executed by identifying all triangles that share a given unique point. This
means that the determination of the neighbors of a given triangle only involves the trian-
gles that are part of the point clusters that result from the vertices of the given triangle.
A triangle is then determined as the given triangle’s neighbor if they have two points in
common. Note that the computational expense per triangle is independent of the overall
number of triangles and scales solely with the number of triangles connected to the given
Figure 4: Essential data structure elements for the surface mesh preparation, exemplified by a unit cube. Sorting lists ($\tilde{L}$ & $\tilde{L}_{\text{points}}$) of vertices spatially allows the extraction of unique points ($L$) and identification of point clusters, i.e., triangles that share the same point. The information is stored in arrays ($A_p$ & $A_l$) for faster unordered access.
triangle’s vertices. The process for a given triangle (parent) can be summarized as:

1. For each of the parent’s vertices, calculate the longID from the parent’s sequential ID \{0\ldots n_{\text{Triangles}}\} and the local vertex number \{0\ldots 2\}.

2. Access \(A_l\) at the entry determined above (longID), acquiring the shortID (sequential ID of the first point in the cluster) of the vertex.

3. Enter \(A_l\) at the shortID to get the sequential ID \(i\) of the first point in the cluster.

4. Sequentially loop over all the points of the cluster and use their longID to derive the sequential ID \{0\ldots n_{\text{Triangles}}\} of the triangle (sibling) they belong to.

5. Determine the number of points that the sibling and the parent share by comparing their vertices’ respective newIDs.

6. If a sibling shares two points with the parent, set it as a neighbor of the parent.

For the sake of an example, consider \(i_{\text{Triangle}} = 5\) and \(i_{\text{Vertex}} = 2\) yielding longID \(= 3 \times 5 + 2 = 17\) and thus acquiring shortID = 8 and newID = 7 from the \(A_l\) array (step 1 in Fig. 4). The shortID = 8th element in the \(A_l\) array (step 2) provides the \(i = 31\) value of the point cluster’s first element (step 3). Its longID is 8 resulting in \(j_{\text{Triangle}} = 2\) (step 4). This sibling has vertices with longID = \(j_{\text{Triangle}} \times 3 + \{0, 1, 2\} = \{6, 7, 8\}\) and newID = \{4, 6, 7\}. The parent triangle has vertices with longID = \{15, 16, 17\} and newID = \{6, 3, 7\}. Parent and sibling triangle share the two unique points 6 and 7 (see identical newIDs), which makes them neighbors. Since the shared points are along the parent’s third edge the value \(j_{\text{Triangle}} = 2\) is stored in the parent’s third neighbor entry. This example shows the determination of just one of the three neighbors.

These mesh preparations only have to be carried out once, before the actual grid generation takes place.

### 3.2 Mesh refinement

Since each computational thread operates on the assigned triangle’s bounding box, the size of the triangle is directly affecting the execution time of each thread. Smaller triangles yield smaller bounding boxes resulting in a shorter execution time. Size in this context was chosen to be judged by the triangle’s longest edge, since this attribute predominantly dictates the bounding box’s dimensions. Moving bodies could rotate in such a way that the longest edge is aligned in the least preferable 45° angle with respect to the grid axes. It is therefore desirable to divide the tessellated surface into smaller, i.e. shorter longest edge, and consequently more triangles. To this end a mesh refinement procedure has been developed.

A triangle splitting approach has been chosen. Each triangle with an edge exceeding a set margin is cut in two, generating two new triangles. The blue triangle in Fig. 5 has an edge exceeding the margin and is cut in two. Since hanging nodes would result
in undefined neighbor assignment, the (red) triangle that is sharing the blue triangle’s longest edge is also split.

A new node is generated in the middle of the splitting edge and appended to the list of vertices. The blue and the red triangle are considered to have changed in size and remain at their position in the triangle list while the two new triangles are appended to it. Depending on the edge along which the triangles are halved, the neighbor IDs have to be updated accordingly. All eight triangles in Fig. 5 require neighbor updates because a single triangle was supposed to be split.

4 Details of the ePiP grid generation algorithm

After these initial preparations, the actual grid generation process is triggered. In the following section, the details of the proposed grid generation algorithm are discussed. A grid point is considered part of the inner body shell if

1. it is located on the inside of the triangle as defined by its unit normal vector $\hat{n}$ and its perpendicular distance $s$ to the triangle plane is less than the first layer thickness $\delta$, which is a function of the triangle orientation with respect to the grid, and

2. it is on the inside of all triangle separation planes to the (at most) three adjacent triangles defined by the bisection vectors $b_i$.

4.1 Determination of first layer thickness $\delta$ and distance check

For each triangle a specific layer thickness has to be computed so that all the points that will be identified as inner points have at least one neighbor outside the triangle, i.e. each
inner point really is the first point on the inside. This layer thickness is derived as follows: Consider a cube of grid points (8 points in 3D, 4 points in 2D) and the triangle in question arranged in such a way that one of the cube’s vertices is located on the triangle plane. The cosine of the angle $\varphi$ in-between the triangle normal vector $n = (n_x n_y n_z)^T$, $\|n\| = 1$ and the diagonal $z = (\pm \Delta \pm \Delta \pm \Delta)^T$ of the cube that is oriented positively in the same quadrant as the normal is

$$
cos \varphi = \frac{<z, n>}{\sqrt{3}\Delta} = \frac{\pm \Delta n_x \pm \Delta n_y \pm \Delta n_z}{\sqrt{3}\Delta}. \quad (4.1)
$$

All sub sums of the inner product are positive since the diagonal is oriented towards the same quadrant as the normal vector. This yields

$$
cos \varphi = \frac{|n_x| + |n_y| + |n_z|}{\sqrt{3}}. \quad (4.2)
$$

The layer thickness $\delta$ is the projection of the diagonal to the normal, thus:

$$
\delta = <z, n> = \sqrt{3}\Delta \cos \varphi = \Delta (|n_x| + |n_y| + |n_z|). \quad (4.3)
$$

In Fig. 6, the two-dimensional case is illustrated. In analogy to the three-dimensional, more general case, an equivalent 2D layer thickness of $\delta_{2D} = \Delta (|n_x| + |n_y|)$ is found.

![Figure 6: Determination of the layer thickness in 2D.](image)

The perpendicular distance of a grid point to the triangle plane now is computed via the negative inner product of a vector originating at one of the triangle’s vertices and pointing at the grid point in question and the unit normal vector. The negative inner product yields the norm of the projection of the point vector onto the inverted normal and therefore the desired distance:

$$
s = - <v_4, n> = - <v_5, n> = - <v_6, n>, \quad (4.4)
$$
where \( v_{3+i} \) with \( i = 1,2,3 \) are defined as vectors originating from \( P_i \) and pointing at the grid point in question, \( P_4 \):

\[
v_{3+i} = P_4 - P_i. \tag{4.5}
\]

The perpendicular distance \( s \) has to be computed for every point within a triangle’s bounding box. Points that satisfy the condition

\[
s \in [0 : \delta) \tag{4.6}
\]

are identified and further investigated for their validity.

### 4.2 Construction of separation planes

Consider a tessellated closed body topology where each triangle \( t \) has the vertices \( P_1, P_2, \) and \( P_3 \). These points are arranged in such a way that the edges \( v_1 = P_2 - P_1, v_2 = P_3 - P_2, v_3 = P_1 - P_3 \) define an outward pointing normal vector \( \hat{n} = v_1 \times v_2 \). A separation plane is constructed for each triangle edge that is shared with an adjacent neighbor. The unit normal vectors of the focus triangle and its considered neighbor are \( n \) and \( n_{N,i} \), respectively, where \( i = 1,2,3 \) indicates the edge along which the neighbor with unit normal \( n_{N,i} \) occurs. The most straightforward approach would be to construct a symmetrical separation plane \( S_{h,i} \) from the average of the adjacent triangles’ unit normals. Such a plane is defined by the shared triangle edge \( v_i \) and the half-vector

\[
h_i = -n - n_{N,i}. \tag{4.7}
\]

This approach resembles the half-angle definition for \( \alpha \) introduced above, see the left panel in Fig. 7 for the 2D equivalent. Due to the varying layer thickness of the neighboring triangles, this approach is not able to cover the entire inner domain. A triangular prism at the innermost corner of the triangle domains is not tested for either of the neighbors. This can be avoided by introducing the bisection plane \( S_{b,i} \) constructed from the shared triangle edge \( v_i \) and a vector \( b_i \) that is proportional to a vector originating from the edge and pointing towards the intersection line of the two layer thickness planes while being perpendicular to \( v_i \). The layer thickness adjusted bisection approach is depicted in the right panel of Fig. 7. The bisecting vector \( b_i \) is defined as

\[
b_i \propto \tilde{b_i} = \frac{\delta_{N,i}}{\sin \mu} n^\perp + \frac{\delta}{\sin \mu} n_{N,i}^\perp, \tag{4.8}
\]

where \( \delta \) and \( \delta_{N,i} \) are the layer thicknesses of the focus and the neighbor triangle, respectively, and \( n^\perp = n \times v_i \) and \( n_{N,i}^\perp = v_i \times n_{N,i} \) are vectors perpendicular to the triangles’ unit normals and their shared edge (see Fig. 8).

The computation of the sine function can be avoided by multiplying equation (4.8) with \( \sin \mu \) since only the signed direction of \( b_i \) is of interest for the construction of \( S_{b,i} \).
Figure 7: Definition of an unsymmetrical separation plane $S_{b,j}$ (right). This allows the consideration of grid points lying outside (green region) the triangle domains defined by the half-separation plane $S_{h,j}$ (left).

Figure 8: Construction of the unsymmetrical bisection vector $b_i$ from the normal vectors and the shared triangle edge.

For total opening angles $\phi$ of more than $180^\circ$, i.e. concave surface regions, the bisecting vector has to be inverted. The inner product of the edge vector $v_i$ and the vector resulting from the normals’ cross product can be used to define the sign of $b_i$:

$$b_i = < n \times n_{N,j}, v_i > \left[ \delta_{N,i} (n \times v_i) + \delta (v_i \times n_{N,j}) \right].$$

(4.9)

The bisecting vector’s norm is vanishing for $n = n_{N,j}$. In such cases, one possibility is to fall back to the half-vector $h_i$ which actually is the exact solution for this scenario. The separation planes have to be constructed for every edge whenever the surface’s orientation relative to the grid changes. For transient CFD simulations of moving bodies this is the case at every time step.

The grid point $P_3$ is set as a boundary node if

$$< b_i \times v_{3+i}, v_i > \geq 0 \quad \forall \ i \in \{1, 2, 3\}$$

(4.10)
holds true. Fig. 9 depicts the geometrical relations found for the focus triangle and its neighbor: a point is considered part of the domain if it is within each of the triangle’s separation planes. Each plane is defined by the bisection vector \( b_i \). The point is considered inside of separation plane \( S_{b,i} \) if the cross product’s projection of \( b_i \) and \( v_{3+i} \) onto the edge vector \( v_i \) is positive.

For the case that two neighboring triangles almost coincide, i.e. \( \phi \) is very small, all the involved vectors \( b_i, v_{3+i}, \) and \( v_i \) will still be numerically identical (except for the sign in case of \( v_i \)) for the two triangles. Thus, if one of the two triangles yields a positive value for \( \langle b_i \times v_{3+i}, v_i \rangle \) the other triangle will get a negative result, even for very small values of \( b_i \times v_{3+i} \).

5 GPU implementation and performance

The main aim of the proposed voxelization algorithm is to find a surface voxelization corresponding to a tessellated mesh and a rectilinear grid of points in a highly parallelized manner with very few operations necessary. The presented surface voxelization algorithm has been implemented in C++ and CUDA for parallel execution on NVIDIA GPUs. Both single and double precision computations are supported. The CUDA philosophy is based on a combined architecture of a host and one or multiple devices. The host part consists of a CPU and the host memory, whereas the devices are graphics cards with their GPUs and the device memory. Intercommunication between host and device is provided by the PCI-express (PCIe) interface. Just like regular code for single thread processing on the host is compiled with a C compiler, the files with CUDA extensions have to be compiled with the NVIDIA CUDA C compiler (nvcc). A typical CUDA program provides data for processing on the GPU by copying it from host to device memory. The GPU then executes the instructions provided by the host and stores the result data...
in device memory. The presented implementation performs the data copying only once at the beginning of the simulation and relies entirely on the fast-access device memory for following voxelization intervals. The ePiP algorithm is seamlessly embedded into the efficient lattice Boltzmann solver ELBE, a CUDA-accelerated CFD solver based on an efficient Lattice Boltzmann method, that operates on device memory only and hence allows for a very efficient coupling to the grid generator.

5.1 Cost function approach

Before the actual performance measurements on recent GPGPU hardware, some theoretical remarks on the expected performance of ePiP are given. Consider the per triangle computational cost function

\[ C(n_{B}) = k + \lambda n_{B} \]  \hspace{1cm} (5.1)

being a linear function of the number of grid points \( n_{B} \) found in that triangle’s bounding box. The constant per triangle cost \( k \) and per grid point cost \( \lambda \) are parameters of the cost function. The dimensions of the bounding box depend on the triangle’s axes projected and normalized dimensions \( l_{x}, l_{y}, \) and \( l_{z} \) measured in grid units:

\[ n_{B}(l_{x}, l_{y}, l_{z}) = (l_{x} + \Delta B)(l_{y} + \Delta B)(l_{z} + \Delta B). \]  \hspace{1cm} (5.2)

The bounding box overlap \( \Delta B \) is the number of additional points in each direction, introduced to guarantee that a closed inner shell of points can be identified. Therefore, for constant factors \( k \) and \( \lambda \), the cost function can be expressed as a function of the triangle dimensions:

\[ C(l_{x}, l_{y}, l_{z}) = k + \lambda (l_{x} + \Delta B)(l_{y} + \Delta B)(l_{z} + \Delta B). \]  \hspace{1cm} (5.3)

Mesh refinement causes a change in cost according to

\[ C(l_{x}, l_{y}, l_{z}) \rightarrow 2 C(f_{x}l_{x}, f_{y}l_{y}, f_{z}l_{z}), \]  \hspace{1cm} (5.4)

where \( f_{x}, f_{y}, \) and \( f_{z} \) range from 0.5 to 1 depending on the triangle’s shape and orientation. I.e. a refinement step leads to performance gains if the cost of two triangles with smaller dimensions is less than the cost of the single triangle with larger dimensions. Assuming that the number of triangles exceeds the number of cores on the GPU\(^{\dagger} \), a performance maximum is observed if

\[ C(l_{x}, l_{y}, l_{z}) = 2 C(f_{x}l_{x}, f_{y}l_{y}, f_{z}l_{z}). \]  \hspace{1cm} (5.5)

For an isosceles triangle with diminishing area (shape) and \( l_{z} = l_{y} = l_{z} \) (orientation), i.e. a three dimensional diagonal, the dimension factors \( f_{x}, f_{y}, \) and \( f_{z} \) approach the minimum of 0.5. In this case the performance maximum condition ((5.5) with (5.3)) reads

\[ \frac{3}{4} l_{x,y,z}^3 + 6 l_{x,y,z}^2 - 64 - \frac{k}{\lambda} = 0, \]  \hspace{1cm} (5.6)

\( ^{\dagger} \)If the number of triangles is significantly less than the number of cores, i.e. after a refinement step there still is at least one core per triangle, a mesh refinement step will always have a positive effect on performance.
where the bounding box overlap has been set to $\Delta B = 4$. The optimal projected normalized length is $l_{x,y,z}^{\text{optimal}} \approx 3.3$ for $\frac{1}{4} = 30$. This value for $\frac{1}{4}$ has been estimated by comparing the number of algorithmic operations involved per triangle and per grid point. Thus, the optimal normalized triangle edge size is

$$L_{\text{diagonal}}^{\text{optimal}} = \sqrt{3} l_{x,y,z}^{\text{optimal}} \approx 6.$$  

(5.7)

For other triangle shapes and orientations the dimension factors $f_x$, $f_y$, and $f_z$ are more than 0.5 and, therefore, the optimal triangle edge size is also more than it is for the most beneficial case of a three dimensional diagonal:

$$L^{\text{optimal}} \geq 6.$$  

(5.8)

5.2 Performance measurements

Following these theoretical remarks, performance measurements are carried out. To give performance predictions for the grid mapping process of arbitrarily shaped, complex triangular surface meshes, the following predictor function $T$ is used:

$$T(n_\Delta, n_{bb}, n_{active}) = n_\Delta T_\Delta + n_{bb} T_{bb} + n_{active} T_{active} + \frac{n_{active}}{n_\Delta} T_0.$$  

(5.9)

The total computational costs for the grid mapping of one specific triangular surface mesh is assumed to be a function of the total number of triangles $n_\Delta$, the number of grid nodes in the triangles’ bounding boxes, $n_{bb}$, and the number of active nodes, $n_{active}$, that have been finally marked as solid body grid nodes. For the calibration, several parameter studies for a simple spherical geometry (Fig. 10) were conducted, on consecutively refined grids. The performance of the grid generator was analyzed in terms of MNAPS (million node activations per second),

$$P_{ePiP} = \frac{n_{active}}{T}.$$  

(5.10)

The grid mapping was repeated 100 times in a row, to get a reliable estimate for the computational time $T$. All the computations were done on an NVIDIA GTX Titan GPGPU board, that was connected to a multi-core host machine via an external PCI-Express expansion chassis. Single precision arithmetics and CUDA 5.5 [22] were used.

In Fig. 11, the resulting performance is plotted, against the number of active nodes per triangle. As expected, and predicted by the cost function model in the previous section, the performance maximum is achieved for $L^{\text{optimal}} \geq 6$, corresponding to a number of active nodes per triangle of approximately 36. Secondly, an increasing performance with increasing total number of triangles can be observed. Apart from parallelization issues, this reflects the fact that, with increasing number of triangles, the overhead of inactive nodes in the triangle bounding boxes decreases (which is accounted for by the $T_0$-term in
Figure 10: Illustration of the spherical test geometry that was used for the calibration of the ePip performance prediction model. Exemplarily, four selected refinement levels with 160, 720, 3040, and 12480 nodes are displayed.

Figure 11: 3D ePip sphere test case — predicted (-) and measured (x) performance plotted against the number of active nodes per triangle for six different configurations from 720 to 816,640 triangles.

(5.9)). The test case with the highest number of triangles ($n_\Delta = 816,640$) has been used to calibrate the unknown factors $T_i$ in the performance prediction model as follows:

$$T_\Delta = 5.85E-9s, \quad T_{active} = 1.38E-9s, \quad T_{bb} = 5.64E-11s, \quad T_0 = 1.6E-6s. \quad (5.11)$$

The resulting model then has been used to predict the runtimes of the remaining test cases (with 720 to 203,520 triangles), and a very good agreement was found, see Fig. 11 (symbols vs. straight lines). For all the test cases, the performance of the grid generation tool was between 10 and 100 million node activations per second.

Fig. 12 shows the inverse relation, i.e. the computational time for the grid mapping over the number of triangles per activated node. Even in the worst case of low numbers of triangles per active node, the total computational costs of the grid generation is below 0.1 seconds.
5.3 Performance prediction for the Stanford bunny

The performance prediction model can be used to estimate the computational time for the grid generation process in a real-world problem with a more complex configuration. As
benchmark geometry, the well-established Stanford Bunny is examined [23]. The Stanford Bunny has been used extensively in various research projects to test geometric algorithms. The digital bunny model was created by a range scan technique [24]. While the original bunny model has 69,451 triangles only, we use an enriched version with 270,000 triangles. The test geometry has a bounding box of 50 times 38.7 times 49.5 length units (LU) cubed and a surface area of 5842 LU squared. The bunny geometry is mapped to a Cartesian, equidistant grid with (at most) 10 lattice nodes per unit length, yielding a computational grid of 95.8 million nodes, and a discrete bunny surface of approximately 584,200 lattice nodes, corresponding to \( n_{\text{active}} \approx 2.163 \). With the help of Fig. 11, Eq. (5.9) and the calibration factors in Eq. (5.11), a performance maximum of 100 MNAPS is predicted. In Fig. 14, the performance prediction and the actual measured performance are compared for different grid sizes. Indeed, the resulting performance maximum is found to be approximately 80 MNAPS, so that the prediction is rather accurate and the extrapolation of calibration data from convex and ideal spherical geometries to a lot more complex, concave geometries is valid.

### 5.4 Coupling to CFD solvers

In CFD applications, not only the absolute grid generation time is of interest, but the ratio of computational time for the actual CFD computations and the geometry updates. For the above-mentioned bunny grid, the computational costs for one Lattice-Boltzmann time step can easily be estimated and/or found in literature. In our experience, the maximum performance of three-dimensional GPGPU-accelerated LBM solvers is in the order of 250 MNUPS (million grid node updates per second) for complex multiphysics simu-
lations, e.g. free surface flow simulations [25], and up to 500 MNUPS for singlephase flow problems [26] including complex geometries, boundary conditions and grid updates. Our performance estimate is based on our experiences with the ELBE code, for complex singlephase flows. However, for academic benchmark problems (e.g. lid-driven cavity, simple boundary conditions, domain sizes a power of two) with simple collision operators, several publications report a performance of up to 1.4 GNUPS on a GTX Titan board. This can be reproduced by the ELBE code for very academic settings. The performance decreases as soon as more complex collision operators, such as an MRT-LES, and more complex boundary conditions, such as LIBB or anti-bounce-back pressure boundaries, are used. Moreover, we found that, as soon as the grid sizes are not a power of two, performance decreases remarkably (e.g. comparing a 256 cubed grid with a 300 cubed grid). If we then further consider fluid-structure interaction problems, where the presented grid generator comes into play and a dynamic update of the Eulerian grid is necessary, additional force calculation, local boundary conditions and a refill algorithm for the grid nodes that change from solid to fluid are mandatory in the flow field update. Nevertheless, even for a 1,400 MNUPS fluid solver, the performance of ePiP still is more than sufficient: the theoretical performance of the coupled solver, including the LBM flow field update and the subsequent ePiP grid update, yields

\[
P_{\text{total}} = \frac{n_{\text{total}}}{1,400 \text{ MNUPS}} + \frac{80 \text{ MNAPS}}{95.8E6} = \frac{95.8E6}{1,400 \text{ MNUPS}} + \frac{5.8E5}{80 \text{ MNAPS}} \approx 1,265 \text{ MNUPS.}
\]  

Hence, the overhead of the grid update step decelerates the numerical simulation by only 9.6%. Re-calcualting the overhead based on the above-mentioned more realistic ELBE performance values even reduces the overhead to 3.6%, which is deemed to be negligible.

5.5 Applications

After the successful validations, coupled algorithm can be applied to state-of-the-art problems in computational fluid dynamics, including the numerical simulation of fluid-structure interactions and flows in complex geometries. Application examples include thermal cabin flows (Fig. 15), multiphase flows (Fig. 16) and aircraft ditching simulations (Fig. 17). Further application examples with moving geometries and more complex fluid-structure interactions are currently under development and will be published in the near future.

6 Conclusions and outlook

In this paper, a novel, fast and efficient parallel surface voxelization technique was presented. The necessary preprocessing steps, key concepts of the parallel implementation and performance results were addressed. The computational surplus of the presented
Figure 15: Snapshot of a three-dimensional ELBE singlephase flow simulation: developing flow field (velocity magnitude) in an A380 cabin mock-up [27].

Figure 16: Snapshot of a three-dimensional ELBE multiphase flow simulation: phase interface location in a complex 3D stirring unit (left: surface mesh representation of the unit, right: corresponding surface voxelization).

Figure 17: Snapshot of a three-dimensional ELBE freesurface flow simulation: ditching of an aircraft fuselage.
grid generation procedure was shown to be negligible in comparison with the performance of the flow field calculations. The coupled algorithm allows for very efficient fluid-structure interaction simulations, without noticeable performance loss due to the dynamic grid update. More specifically, the (theoretical) performance loss is found to be less than 10%, even for highly-accelerated GPU-LBM solvers.

Due to these very promising numerical results and the very competitive performance, the present methodology will be further examined and improved in future work. To enhance the numerical accuracy of the boundary representation, second-order boundary conditions will be addressed. Representing an arbitrary surface with a voxelized Cartesian grid introduces discretization errors that scale with the grid spacing and results in numerical simulations with first order accuracy. Second-order accuracy can be obtained by improved boundary conditions based on an additional consideration of the surface distances for the identified body nodes and their respective lattice directions. In the context of LBM, these distances are often referred to as subgrid distances [28]. By design, the extension of the presented methodology to calculate such distances is straightforward.

The left panel of Fig. 18 depicts a circle that is discretized with 360 line elements. In the right panel, the red nodes indicate the nodes inside the axis-aligned bounding box of the red line element, that are tested with the ePiP algorithm. The thin radials mark the unique element domains. The bright green node is identified as a solid body node by the thread that has been assigned to the red line element. The eight discrete lattice directions (thick, light gray) are intersected with the line segment’s extended line (thin, red) to compute the node distance (thick, turquoise). A reliable implementation for the computation of the lattice distances in 3D is considerably more computationally expensive and an efficient technique for their determination has to be found in order to realize higher order boundary conditions for smoother body representation on Cartesian grids for the lattice Boltzmann method.

Figure 18: (Left) A circle (red), which is discretized with 360 line elements, is mapped to the Cartesian grid. Cubes colored in shades of gray indicate their proximity to the circle’s surface (black = internal or close by; white = distant). (Right) A detailed depiction of a line segment (red line) with its bounding box (red dots), and the computed lattice distances (turquoise).
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References

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